

# GOVERNMENT POLYTECHNIC YAVATMAL

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No.GPY/Estt/Ad/2018/ 8840

Date 27/12/19

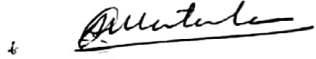
## Walk in Interview

Visiting faculty on purely temporary clock hour basis is required for Electronics & Tele.Comm.Engg.

Qualification as per AICTE norms

Date & Time of Interview 04-01.2019 , 12.00 Noon am Venue ; - Electronics Engg.

Department Intersted candidats shall remain present with application all original documents & One set of Photocopy



Principal

(Government Polytechnic Yavatmal Old)



